

CENG3420

Lab 1-1: MIPS assembly language programming

Haoyu Yang

Department of Computer Science and Engineering
The Chinese University of Hong Kong

hyyang@cse.cuhk.edu.hk

Spring 2020



香港中文大學
The Chinese University of Hong Kong

Overview

SPIM

Assembly Programing

System Service in SPIM

Lab Assignment



Overview

SPIM

Assembly Programing

System Service in SPIM

Lab Assignment



What is SPIM

- ▶ **SPIM is a MIPS32 simulator.**
- ▶ *Spim* is a self-contained simulator that runs MIPS32 programs.
- ▶ It reads and executes assembly language programs written for this processor.
- ▶ *Spim* also provides a simple debugger and minimal set of operating system services.
- ▶ *Spim* does not execute binary (compiled) programs.

Download it here:

<http://sourceforge.net/projects/spimsimulator/files/>



Register Pane and Memory Panel

The screenshot displays the Qt5pim application interface. The top menu bar includes File, Simulator, Registers, Text Segment, Data Segment, Window, and Help. Below the menu bar is a toolbar with various icons. The main window is divided into three panes:

- Register pane:** Located on the left, it shows a list of registers (FP Regs, nt Regs [16], Data, Text) and their values. The registers are listed from \$F12 to \$F29. The register pane is highlighted with a red box and labeled "Register pane".
- Memory panel:** Located in the middle, it shows the memory segment [00400000]..[00440000] and the kernel text segment [80000000]..[80010000]. The memory panel is highlighted with a red box and labeled "Memory panel".
- Message panel:** Located at the bottom, it shows the SPIM Version 9.1.17 of January 1, 2016, copyright 1990-2016, James M. Larus. The message panel is highlighted with a red box and labeled "Message panel".

```
File Simulator Registers Text Segment Data Segment Window Help
FP Regs nt Regs [16] Data Text
FP Regs
F12 = 9800
FCHK = 0
FPCR = 0
FPCRR = 0
FPEX = 0
Single Precision
F00 = 0
F01 = 0
F02 = 0
F03 = 0
F04 = 0
F05 = 0
F06 = 0
F07 = 0
F08 = 0
F09 = 0
F10 = 0
F11 = 0
F12 = 0
F13 = 0
F14 = 0
F15 = 0
F16 = 0
F17 = 0
F18 = 0
F19 = 0
F20 = 0
F21 = 0
F22 = 0
F23 = 0
F24 = 0
F25 = 0
F26 = 0
F27 = 0
F28 = 0
F29 = 0
[00400000] 8fa40000 lw $4, 0($29)
[00400004] 27a50004 addiu $5, $29, 4
[00400008] 24a60004 addiu $6, $5, 4
[0040000c] 00041080 sll $2, $4, 2
[00400010] 00c23021 addu $6, $6, $2
[00400014] 0c100009 jal 0x00400024 [main]
[00400018] 00000000 nop
[0040001c] 3402000a ori $2, $0, 10
[00400020] 0000000c syscall
[00400024] 340a0019 ori $10, $0, 25
[00400028] 3c011001 lui $1, 4097
[0040002c] 8c2b0000 lw $11, 0($1)
[00400030] 014b4020 add $12, $10, $11
[00400034] 014b4822 sub $13, $10, $11
[00400038] 3c011001 lui $1, 4097 [msg]
[0040003c] 34240004 ori $4, $1, 4 [msg]
[00400040] 0000000c syscall
[00400044] 3402000a ori $2, $0, 10
[00400048] 0000000c syscall
Kernel Text Segment [80000000]..[80010000]
[80001800] 0001d821 addu $27, $0, $1
[80001840] 3c019000 lui $1, -28672
[80001880] ac220200 sw $2, 512($1)
[800018c0] 3c019000 lui $1, -28672
[80001900] ac240204 sw $4, 516($1)
[80001940] 401a0800 mf0 $26, $13
[80001980] 001a2082 srl $4, $26, 2
[800019c0] 3084001f andi $4, $4, 31
[80001a00] 34020004 ori $2, $0, 4
[80001a40] 3c040000 lui $4, -28672 [__ml_]
[80001a80] 0000000c syscall
[80001ac0] 34020001 ori $2, $0, 1
[80001b00] 001a2082 srl $4, $26, 2
[80001b40] 3084001f andi $4, $4, 31
[80001b80] 0000000c syscall
SPIM Version 9.1.17 of January 1, 2016
Copyright 1990-2016, James M. Larus.
All Rights Reserved.
SPIM is distributed under a BSD license.
See the file README for a full copyright notice.
QtSPIM is linked to the Qt library, which is distributed under the GNU Lesser General Public License version 3 and version 2.1.
Memory and registers cleared
SPIM Version 9.1.17 of January 1, 2016
Copyright 1990-2016, James M. Larus.
All Rights Reserved.
SPIM is distributed under a BSD license.
See the file README for a full copyright notice.
QtSPIM is linked to the Qt library, which is distributed under the GNU Lesser General Public License version 3 and version 2.1.
```

There's also a console window.



Operations

- ▶ Load a source file: File → Reinitialize and Load File
- ▶ Run the code: F5 or Press the green triangle button
- ▶ Single stepping: F10
- ▶ Breakpoint: in Text panel, right click on an address to set a breakpoint there.



Overview

SPIM

Assembly Programing

System Service in SPIM

Lab Assignment



Registers

- ▶ 32 general-purpose registers
- ▶ register preceded by \$ in assembly language instruction
- ▶ two formats for addressing:
 - ▶ using register number e.g. \$0 through \$31
 - ▶ using equivalent names e.g. \$t1, \$sp
- ▶ special registers Lo and Hi used to store result of multiplication and division
 - ▶ not directly addressable; contents accessed with special instruction `mghi` (“move from Hi”) and `mfl0` (“move from Lo”)



Register Names and Descriptions

Name	Register Number	Usage	Preserve on call?
\$zero	0	constant 0 (hardware)	n.a.
\$at	1	reserved for assembler	n.a.
\$v0 - \$v1	2-3	returned values	no
\$a0 - \$a3	4-7	arguments	yes
\$t0 - \$t7	8-15	temporaries	no
\$s0 - \$s7	16-23	saved values	yes
\$t8 - \$t9	24-25	temporaries	no
\$gp	28	global pointer	yes
\$sp	29	stack pointer	yes
\$fp	30	frame pointer	yes
\$ra	31	return addr (hardware)	yes



Data Types and Literals

Data types:

- ▶ Instructions are all 32 bits
- ▶ byte(8 bits), halfword (2 bytes), word (4 bytes)
- ▶ a character requires 1 byte of storage
- ▶ an integer requires 1 word (4 bytes) of storage
- ▶ Data types: `.asciiz` for string, `.word` for int, ...

Literals:

- ▶ numbers entered as is. e.g. 4
- ▶ characters enclosed in single quotes. e.g. 'b'
- ▶ strings enclosed in double quotes. e.g. "A string"



Program Structure I

- ▶ Just plain text file with data declarations, program code (name of file should end in suffix `.s` to be used with SPIM simulator)
- ▶ Data declaration section followed by program code section

Data Declarations

- ▶ Identified with assembler directive **.data**.
- ▶ Declares variable names used in program
- ▶ Storage allocated in main memory (RAM)
- ▶ `<name>: .<datatype> <value>`



Program Structure II

Code

- ▶ placed in section of text identified with assembler directive **.text**
- ▶ contains program code (instructions)
- ▶ starting point for code e.g. execution given label **main:**
- ▶ ending point of main code should use exit system call

Comments

anything following # on a line



Program Structure III

The structure of an assembly program looks like this:

Program outline

```
# Comment giving name of program and description
# Template.s
# Bare-bones outline of MIPS assembly language program

    .globl main

    .data    # variable declarations follow this line
            # ...

    .text    # instructions follow this line

main:      # indicates start of code
          # ...

# End of program, leave a blank line afterwards
```



An Example Program

```
1      .globl main
2      .data
3  msg: .asciiz "Welcome to CENG3420.\n"
4      .text
5  main:
6      li $v0,4
7      la $a0,msg
8      syscall
9      li $v0,10
10     syscall
11
```

- ▶ `li`: load immediate
- ▶ `la`: load address
- ▶ `lw`: load word from memory



More Information

For more information about MIPS instructions and assembly programming you can refer to:

1. Lecture slides and textbook.

2. `http:`

`//www.mrc.uidaho.edu/mrc/people/jff/digital/MIPSir.html`



Overview

SPIM

Assembly Programing

System Service in SPIM

Lab Assignment



System calls in SPIM I

SPIM provides a small set of operating system-like services through the system call (`syscall`) instruction.

Service	System call code	Arguments	Result
<code>print_int</code>	1	<code>\$a0 = integer</code>	
<code>print_float</code>	2	<code>\$f12 = float</code>	
<code>print_double</code>	3	<code>\$f12 = double</code>	
<code>print_string</code>	4	<code>\$a0 = string</code>	
<code>read_int</code>	5		integer (in <code>\$v0</code>)
<code>read_float</code>	6		float (in <code>\$f0</code>)
<code>read_double</code>	7		double (in <code>\$f0</code>)
<code>read_string</code>	8	<code>\$a0 = buffer, \$a1 = length</code>	
<code>sbrk</code>	9	<code>\$a0 = amount</code>	address (in <code>\$v0</code>)
<code>exit</code>	10		
<code>print_char</code>	11	<code>\$a0 = char</code>	
<code>read_char</code>	12		char (in <code>\$v0</code>)
<code>open</code>	13	<code>\$a0 = filename (string), \$a1 = flags, \$a2 = mode</code>	file descriptor (in <code>\$a0</code>)
<code>read</code>	14	<code>\$a0 = file descriptor, \$a1 = buffer, \$a2 = length</code>	num chars read (in <code>\$a0</code>)
<code>write</code>	15	<code>\$a0 = file descriptor, \$a1 = buffer, \$a2 = length</code>	num chars written (in <code>\$a0</code>)
<code>close</code>	16	<code>\$a0 = file descriptor</code>	
<code>exit2</code>	17	<code>\$a0 = result</code>	



System calls in SPIM II

To request a service, a program loads the system call code into register $\$v0$ and arguments into registers $\$a0$ – $\$a3$ (or $\$f12$ for floating-point values). System calls that return values put their results in register $\$v0$ (or $\$f0$ for floating-point results). Like this example:

Using system call

```
.data
str: .asciiz "the_answer_=_ " #labels always followed by colon
.text

li    $v0, 4    # system call code for print_str
la    $a0, str  # address of string to print
syscall # print the string
li    $v0, 1    # system call code for print_int
li    $a0, 5    # integer to print
syscall # print it
```



Overview

SPIM

Assembly Programing

System Service in SPIM

Lab Assignment



Lab Assignment

Write an assembly program with the following requirements:

1. Define two variables `var1` and `var2` which have initial value 15 and 19, respectively.
2. Print RAM addresses of `var1` and `var2` using `syscall`.
3. Increase `var1` by 1 and multiply `var2` by 4.
4. Print `var1` and `var2`.
5. Swap `var1` and `var2` and print them.

Submission Method:

Submit the source code and report **after** the whole Lab1, onto [blackboard](#).



Some Tips

1. Variables should be declared following the `.data` identifier.
2. `<name>: .<datatype> <value>`
3. Use `la` instruction to access the RAM address of declared data.
4. Use system call to print integers.
5. Do not forget `exit` system call.

