[CEG3420 Lab 2 Report]

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| [Your Name] |   | [SID] |

# Task 1

**Describe the outline of how to update NEXT\_LATCHES in process\_instruction() here:**

**Describe the outline of how to implement the function “memWord()” here:**

**Paste your experimental results here by running “nop.cod”, “count10.cod” and “toupper.cod” respectively.**

**(You need paste the information of instructions and registers)**

# Task 2

**Describe the outline of how to implement the function partVal() here:**

**Describe the outline of how to implement function “SEXT()” here:**

**Describe the outline of how to implement function “setCC()” here:**

**Paste your experimental results here by running “testTask2-2.cod”.**

**(You need paste the information of instructions and registers)**

# Task 3

**Describe how you parse OPCODE BR in “case 0” of “process\_instruction()”:**

**Describe how you parse OPCODE LDW in “case 6” of “process\_instruction()”:**

**Describe how you parse OPCODE LEA in “case 14” of “process\_instruction()”:**

**Paste your experimental results here by running “count10.cod”.**

**(You need paste the information of instructions and registers)**

# Task 4

**Describe how you parse OPCODE LDB in “case 2” of “process\_instruction()”:**

**Describe how you parse OPCODE STB in “case 3” of “process\_instruction()”:**

**Paste your experimental results here by running “toupper2.cod”.**

**(You need paste the information of instructions, memory (from 0x4000 to 0x4002) and registers)**