

# CENG4480

# Operational Amplifiers Lab 1:

# Tutorial Session

**Xiangjun Peng**

[xjpeng@cse.cuhk.edu.hk](mailto:xjpeng@cse.cuhk.edu.hk)

<https://shiangjun.cn>

**Memory and Storage System Research Group**

Last Edited in September 16th 2020



香港中文大學

The Chinese University of Hong Kong

# Outline

- **Introduction & Overview**
- **Objectives & Expected Outcome**
- **Assignments & Guidelines**
- **Suggestions**

# Intro. & Overview

- **Recap the Contents:**
  - **Operational Amplifiers** [Lec. 1 & Lec. 2]
- **Hence, the Main Focus would be**
  - **Hands-on Experiments on Operational Amplifiers**
- **We would cover THREE major designs**
  - **Use the Simulator** instead
- **Plz Download it through either:**
  - **OneDrive [\[Link\]](#)**
  - **Source Website [\[Link\]](#)**

# Obj. & Exp. Outcome

- **The Purpose of Labs:**
  - **Getting Your Hands Dirty**
- **Hence, I won't help you beyond the detailed guidelines**
  - **Take a Deep Breadth & Follow the Report**
  - **I would show you a way through one concrete example.**
- **Expected Deliverables**
  - **U have 10 days to finish this (DDL: 23:59, Oct. 1)**
  - **A Report, which consists of**
    - **Three Operational Amplifiers Simulations**
    - **In total, we have FOUR tasks.**

# Assignments & Guides

- **FOUR Experiments:**
  - Setup the Simulator (Sec. 2)
  - Inverting Amplifier (Sec. 3)
  - **Non-inverting Amplifier** (Sec. 4)
  - **Voltage Follower** (Sec. 5)
- **All Materials have been Covered:**
  - We would showcase the first two sections
- **Guidelines:**
  - Be Careful **for the Rest of Assignments**
    - Similar Procedure, with Different Configurations

# CENG4480

# Operational Amplifiers Lab 1:

# Tutorial Session

**Xiangjun Peng**

[xjpeng@cse.cuhk.edu.hk](mailto:xjpeng@cse.cuhk.edu.hk)

<https://shiangjun.cn>

**Memory and Storage System Research Group**

Last Edited in September 16th 2020



香港中文大學

The Chinese University of Hong Kong