CENG 3420 Computer Organization and Design

Lecture 04: Performance

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Performance Metrics

- Purchasing perspective
 - given a collection of machines, which has the
 - best performance ?
 - least cost ?
 - best cost/performance?
- Design perspective
 - faced with design options, which has the
 - best performance improvement ?
 - least cost ?
 - best cost/performance?
- Both require
 - basis for comparison
 - metric for evaluation
- Our goal is to understand what factors in the architecture contribute to overall system performance and the relative importance (and cost) of these factors

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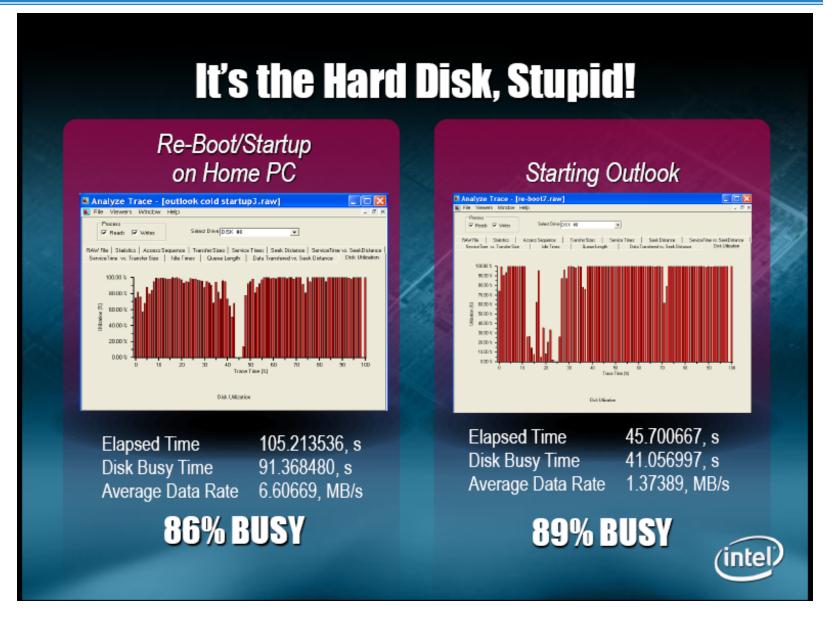
Throughput versus Response Time

- □ Response time (execution time) the time between the start and the completion of a task
 - Important to individual users
- Throughput (bandwidth) the total amount of work done in a given time
 - Important to data center managers

■ Will need different performance metrics as well as a different set of applications to benchmark embedded and desktop computers, which are more focused on response time, versus servers, which are more focused on throughput

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Response Time Matters



Defining (Speed) Performance

■ To maximize performance, need to minimize execution time

If X is n times faster than Y, then

Decreasing response time almost always improves throughput

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A Relative Performance Example

If computer A runs a program in 10 seconds and computer B runs the same program in 15 seconds, how much faster is A than B?

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Relative Performance Example

□ If computer A runs a program in 10 seconds and computer B runs the same program in 15 seconds, how much faster is A than B?

We know that A is n times faster than B if

The performance ratio is
$$\frac{15}{10} = 1.5$$

So A is 1.5 times faster than B

Performance Factors

- CPU execution time (CPU time) time the CPU spends working on a task
 - Does not include time waiting for I/O or running other programs

CPU execution time = # CPU clock cycles x clock cycle time for a program

or

CPU execution time = # CPU clock cycles for a program clock rate

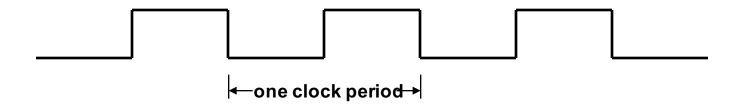
Can improve performance by reducing either the length of the clock cycle or the number of clock cycles required for a program

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Review: Machine Clock Rate

 Clock rate (clock cycles per second in MHz or GHz) is inverse of clock cycle time (clock period)

$$CC = 1/CR$$



10 nsec clock cycle => 100 MHz clock rate

5 nsec clock cycle => 200 MHz clock rate

2 nsec clock cycle => 500 MHz clock rate

1 nsec (10^{-9}) clock cycle => 1 GHz (10^9) clock rate

500 psec clock cycle => 2 GHz clock rate

250 psec clock cycle => 4 GHz clock rate

200 psec clock cycle => 5 GHz clock rate

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Improving Performance Example

□ A program runs on computer A with a 2 GHz clock in 10 seconds. What clock rate must a computer B run at to run this program in 6 seconds? Unfortunately, to accomplish this, computer B will require 1.2 times as many clock cycles as computer A to run the program.

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Improving Performance Example

A program runs on computer A with a 2 GHz clock in 10 seconds. What clock rate must a computer B run at to run this program in 6 seconds? Unfortunately, to accomplish this, computer B will require 1.2 times as many clock cycles as computer A to run the program. CPU time_A = CPU clock cycles_A clock rate_A

CPU clock cycles_A = 10 sec x 2 x
$$10^9$$
 cycles/sec = 20×10^9 cycles

CPU time_B =
$$\frac{1.2 \times 20 \times 10^9 \text{ cycles}}{\text{clock rate}_B}$$
 = $\frac{1.2 \times 20 \times 10^9 \text{ cycles}}{6 \text{ seconds}}$ = 4 GHz

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Clock Cycles per Instruction

- Not all instructions take the same amount of time to execute
 - One way to think about execution time is that it equals the number of instructions executed multiplied by the average time per instruction

CPU clock cycles = # Instructions x Average clock cycles for a program = for a program x per instruction

- Clock cycles per instruction (CPI) the average number of clock cycles each instruction takes to execute
 - A way to compare two different implementations of the same ISA

	CPI for this instruction class					
	A B C					
CPI	1	2	3			

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Effective (Average) CPI

Computing the overall effective CPI is done by looking at the different types of instructions and their individual cycle counts and averaging

Overall effective CPI =
$$\sum_{i=1}^{i} CPI_i \times IC_i$$

- Where IC_i is the percentage of the number of instructions of class i executed
- CPI_i is the (average) number of clock cycles per instruction for that instruction class
- n is the number of instruction classes

■ The overall effective CPI varies by instruction mix – a measure of the dynamic frequency of instructions across one or many programs

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Using the Performance Equation

□ Computers A and B implement the same ISA. Computer A has a clock cycle time of 250 ps and an effective CPI of 2.0 for some program and computer B has a clock cycle time of 500 ps and an effective CPI of 1.2 for the same program. Which computer is faster and by how much?

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Using the Performance Equation

□ Computers A and B implement the same ISA. Computer A has a clock cycle time of 250 ps and an effective CPI of 2.0 for some program and computer B has a clock cycle time of 500 ps and an effective CPI of 1.2 for the same program. Which computer is faster and by how much?

Each computer executes the same number of instructions, *I*, so

CPU time_A =
$$I \times 2.0 \times 250 \text{ ps} = 500 \times I \text{ ps}$$

CPU time_B =
$$I \times 1.2 \times 500 \text{ ps} = 600 \times I \text{ ps}$$

Clearly, A is faster ... by the ratio of execution times

$$\frac{\text{performance}_{A}}{\text{performance}_{B}} = \frac{\text{execution_time}_{B}}{\text{execution_time}_{A}} = \frac{600 \text{ x } I \text{ ps}}{500 \text{ x } I \text{ ps}} = 1.2$$

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THE Performance Equation

Our basic performance equation is then

- These equations separate the three key factors that affect performance
 - Can measure the CPU execution time by running the program
 - The clock rate is usually given
 - Can measure overall instruction count by using profilers/ simulators without knowing all of the implementation details
 - CPI varies by instruction type and ISA implementation for which we must know the implementation details

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Determinates of CPU Performance

CPU time = Instruction_count x CPI x clock_cycle

	Instruction_ count	CPI	clock_cycle
Algorithm			
Programming language			
Compiler			
ISA			
Core organization			
Technology			

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Determinates of CPU Performance

CPU time = Instruction_count x CPI x clock_cycle

	Instruction_ count	CPI	clock_cycle
Algorithm	X	X	
Programming language	X	X	
Compiler	X	X	
ISA	X	X	X
Core organization		X	X
Technology			X

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A Simple Example

Ор	Freq	CPI _i	Freq x CPI _i
ALU	50%	1	
Load	20%	5	
Store	10%	3	
Branch	20%	2	
			Σ =

- How much faster would the machine be if a better data cache reduced the average load time to 2 cycles?
- How does this compare with using branch prediction to shave a cycle off the branch time?
- What if two ALU instructions could be executed at once?

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A Simple Example

Ор	Freq	CPI _i	Freq x CPI _i
ALU	50%	1	.5
Load	20%	5	1.0
Store	10%	3	.3
Branch	20%	2	.4
			\sum = 2.2

.5	.5	.25
.4	1.0	1.0
.3	.3	.3
.4	.2	.4
1.6	2.0	1.95

How much faster would the machine be if a better data cache reduced the average load time to 2 cycles?

CPU time new = $1.6 \times IC \times CC$ so 2.2/1.6 means 37.5% faster.

How does this compare with using branch prediction to shave a cycle off the branch time?

CPU time new = 2.0 x IC x CC so 2.2/2.0 means 10% faster

What if two ALU instructions could be executed at once? CPU time new = $1.95 \times IC \times CC$ so 2.2/1.95 means 12.8% faster

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Workloads and Benchmarks

- Benchmarks a set of programs that form a "workload" specifically chosen to measure performance
- □ SPEC (System Performance Evaluation Cooperative) creates standard sets of benchmarks starting with SPEC89. The latest is SPEC CPU2006 which consists of 12 integer benchmarks (CINT2006) and 17 floating-point benchmarks (CFP2006).

www.spec.org

There are also benchmark collections for power workloads (SPECpower_ssj2008), for mail workloads (SPECmail2008), for multimedia workloads (mediabench), ...

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Old SPEC Benchmarks

Integer benchmarks		FP benchmarks		
gzip	compression	wupwise	Quantum chromodynamics	
vpr	FPGA place & route	swim	Shallow water model	
gcc	GNU C compiler	mgrid	Multigrid solver in 3D fields	
mcf	Combinatorial optimization	applu	Parabolic/elliptic pde	
crafty	Chess program	mesa	3D graphics library	
parser	Word processing program	galgel	Computational fluid dynamics	
eon	Computer visualization	art	Image recognition (NN)	
perlbmk	perl application	equake	Seismic wave propagation simulation	
gap	Group theory interpreter	facerec	Facial image recognition	
vortex	Object oriented database	ammp	Computational chemistry	
bzip2	compression	lucas	Primality testing	
twolf	Circuit place & route	fma3d	Crash simulation fem	
		sixtrack	Nuclear physics accel	
		apsi	Pollutant distribution	

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SPEC CINT2006 on Barcelona (CC = 0.4×10^9)

Name	ICx10 ⁹	СРІ	ExTime	RefTime	SPEC ratio
perl	2,1118	0.75	637	9,770	15.3
bzip2	2,389	0.85	817	9,650	11.8
gcc	1,050	1.72	724	8,050	11.1
mcf	336	10.00	1,345	9,120	6.8
go	1,658	1.09	721	10,490	14.6
hmmer	2,783	0.80	890	9,330	10.5
sjeng	2,176	0.96	837	12,100	14.5
libquantum	1,623	1.61	1,047	20,720	19.8
h264avc	3,102	0.80	993	22,130	22.3
omnetpp	587	2.94	690	6,250	9.1
astar	1,082	1.79	773	7,020	9.1
xalancbmk	1,058	2.70	1,143	6,900	6.0
Geometric Mean					11.7

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Comparing and Summarizing Performance

- How do we summarize the performance for benchmark set with a single number?
 - First the execution times are normalized given the "SPEC ratio" (bigger is faster, i.e., SPEC ratio is the inverse of execution time)
 - The SPEC ratios are then "averaged" using the geometric mean (GM)

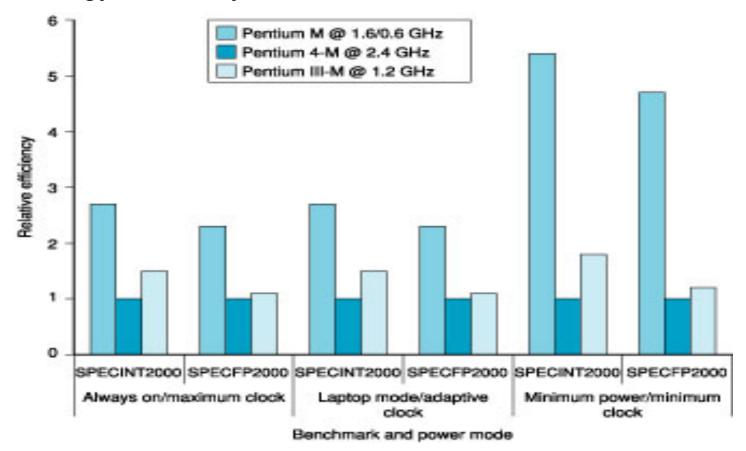
$$GM = n \prod_{i=1}^{n} SPEC \ ratio_{i}$$

□ Guiding principle in reporting performance measurements is reproducibility — list everything another experimenter would need to duplicate the experiment (version of the operating system, compiler settings, input set used, specific computer configuration (clock rate, cache sizes and speed, memory size and speed, etc.))

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Other Performance Metrics

- Power consumption especially in the embedded market where battery life is important
 - For power-limited applications, the most important metric is energy efficiency



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